



PATENT
Intel/17228

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant(s): Wu et al.

Serial No.: 10/747,598

Filed: December 29, 2003

Assignee: Intel Corporation

For: Methods And Apparatus For
Optimizing A Program Undergoing
Dynamic Binary Translation Using Profile
Information

Group Art Unit: Unknown

Examiner: Unknown

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DATED:

April 8, 2004

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
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Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

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The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

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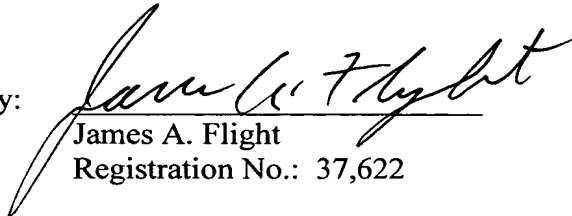
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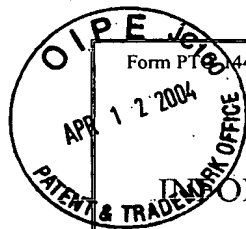
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Attorneys For Intel



Form PTB 449 (Modified)

U.S. Department of Commerce
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INTEL/17228

Serial No.

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Applicant

WU, et al.

Filing Date

12/29/03

Group Art Unit

Unknown

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	INVENTOR(S)	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01					

FOREIGN PATENT DOCUMENTS

*Examiner Initials	Document Number	Publication Date	Country	Class	Subclass	Translation Yes No	
	B01						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

		<i>Advanced Computer Architecture</i> . Ienne, 2003. Slides 1-49. 2003.
		Chilimbi, T.; Hirzel, M. <i>Dynamic Hot Data Stream Prefetching for General-Purpose Programs</i> . Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation, 2002. pp. 199-209.
		Luk, C.; Muth, R.; Patil, H.; Weiss, R.; Lowney, G.; Cohn, R. <i>Profile-Guided Post-Link Stride Prefetching</i> . International Conference on Supercomputing. Proceedings of the 16th international conference on Supercomputing, 2002. pp. 167-178.
		Ghosh, S.; Kanhere, A.; Krishnaiyer, R.; Kulkarni, D.; Li, W.; Lim, C.; Ng, J. <i>Integrating High-Level Optimizations in a Production Compiler: Design and Implementation Experience</i> . Compiler Construction, 2003, April 2003, Warsaw, Poland. pp. 1-19.
		Wu, Y. <i>Efficient Discovery of Regular Stride Patterns in Irregular Programs and Its Use in Compiler Prefetching</i> . Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation, 2002. pp. 210-221.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.